

## Biography

**Ming-Der Shieh** received the B.S. degree in electrical engineering from National Cheng Kung University, Taiwan, in 1984, the M.S. degree in electronic engineering from National Chiao Tung University, Taiwan, in 1986, and the Ph.D. degree in electrical engineering from Michigan State University, East Lansing, in 1993. From 1988 to 1989, he was an engineer at United Microelectronic Corporation, Taiwan. After receiving the Ph.D. degree, he joined the Department of Electronic Engineering, National Yunlin University of Science & Technology (NYUST). Since 2002, he has been with the Department of Electrical Engineering, National Cheng Kung University (NCKU), where he is currently a professor. His research interests include VLSI design and test, VLSI for signal processing, and digital communication.

Dr. Shieh was the Chairman of EE Department at NYUST (1999-2002) and NCKU (2014-2017), and Dean of Miin Wu School of Computing at NCKU from 2021-2022. He also served as the Deputy General Director of Information and Communications Research Laboratories (ICL) at Industrial Technology Research Institute (ITRI) from 2010 to 2014. In addition, he served as the Associated Editor of IEEE Transactions on Circuits and Systems – Part I (2010~2012), and General Chair/Program Chair/Technical Committee Member of several international conferences. Currently, as the Chief Technology Officer of Electronic and Optoelectronic System Research Laboratories at ITRI, he is in charge of the IC research and development, particularly in the area of AI chip design from different aspects, heterogeneous chiplet design and integration, and automotive IC designs.