

Prof. Masahiro Aoyagi

### • Contact Information

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#### Education

- (1) 25 March 1982, Department of Electronic Engineering, School of Engineering, Nagoya Institute of Technology
- (2) 4 June 1992, Department of Electrical and Computer Engineering, Graduate School of Engineering, Nagoya Institute of Technology (Doctoral Degree by Dissertation)

# Professional Experience

- 1st April 1982: Researcher, Electrotechnical Laboratory (ETL), Tsukuba, Japan
- 1st April 1992: Senior Researcher, Electrotechnical Laboratory (ETL), Tsukuba, Japan
- 1st April 2000: Research Group Leader, Nanoelectronics Research Institute (NeRI), National Institute of Advanced Science and Technology (AIST), Tsukuba, Japan
- 1st April 2012: Deputy Director, Nanoelectronics Research Institute (NeRI), National Institute of Advanced Science and Technology (AIST), Tsukuba, Japan
- 1st April 2015: Director of Collaboration Promotion Unit, Tsukuba Innovation Arena (TIA) Central Office, National Institute of Advanced Science and Technology (AIST), Tsukuba, Japan
- 1st April 2019: Invited Researcher (Secretary General of TIA), TIA Central Office, National Institute of Advanced Science and Technology (AIST),

- Tsukuba, Japan
- 1st April 2023: Director (Distinguished Professor), Forefront Research and Education Center for Semiconductors, Faculty of Advanced Science and Technology, Kumamoto University, Kumamoto, Japan
- 1st April 2024 present: Distinguished Professor, Semiconductor Division Research and Education Institute for Semiconductors and Informatics, Kumamoto University, Kumamoto, Japan

#### Honors and Awards

- 1986 The 19<sup>th</sup> Ichimura Academic Award, The Ichimura Foundation for New Technology
- 1991 The 3<sup>rd</sup> Tsukuba Prize, The Science and Technology Promotion Foundation of Ibaraki
- 2006 Technology Award, The Japan Institute of Electronics Packaging
- 2008 The 3<sup>rd</sup> Monozukuri Collaboration Special Award, The Nikkan Kogyo Shinbun

# Topic of Talk

Research and Education Activities for Next-Generation Semiconductor Device & Packaging Technology in Kumamoto University